

ACTERNA

6000/A

The Acterna (formerly TTC) FIREBERD 6000A is a versatile, modular communications analyzer and Bit Error Rate Tester (BERT) designed for comprehensive testing of wide area networks. By utilizing plug-in modules for specific physical interfaces, it provides flexible physical and data link layer analysis. It features a built-in frequency synthesizer, programmable test patterns, and full remote programmability, and is capable of traditional bit error analysis, ATM frame/cell generation, and jitter tolerance testing for T1 and E1 circuits.

SPECIFICATIONS

General

Interfaces	RS-232, IEEE-488 (GPIB) for remote control; Line interfaces via plug-in modules
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System Overview

Product Type	Communications Analyzer / Bit Error Rate Tester (BERT)
Architecture/Modularity	Mainframe (requires plug-in modules for specific line interfaces)
Primary Applications	T1, E1 (2M), Async, Datacom, and ATM testing

Test Interfaces / Line Interfaces

Supported Interface Types	Module dependent (TTC FIREBERD plug-in modules required)
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Transmitter Specifications (Generator)

Clock Source / Timing Modes	Built-in frequency synthesizer
Test Patterns	Programmable user patterns, standard PRBS, fixed words
Flow Control	User-defined flow control

Measurement Capabilities / Results

Measurement Types	Traditional bit error analysis, signal, performance, and timing measurements
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Jitter and Wander Analysis

Jitter Capabilities	Generation and measurement of jitter tolerance on T1 and E1 (2M) circuits
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Framing and Formatting

Payload Mapping	Frame and ATM cell generation
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User Interface and Display

User Input Method	Intuitive, menu-driven front panel
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Connectivity / Remote Control

Remote Control Programmability	Fully programmable via remote interfaces
Data Storage/Export	Customized printouts supported

TECHNOLOGY

T1 / E1 / ATM / Datacom

APPLICATIONS

Wide Area Network (WAN) Installation, Maintenance, and Troubleshooting

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