ANRITSU

MP1764A

The Anritsu MP1764A is a high-performance 12.5 Gb/s Error Detector (ED) designed for Bit Error Rate (BER) testing of high-speed digital communications systems, optical transmission equipment, and semiconductors. Operating across a broad frequency range of 50 MHz to 12.5 GHz, it works in tandem with the MP1763A Pulse Pattern Generator to provide comprehensive testing capabilities for R&D and manufacturing environments. This instrument features a highly legible Fluorescent Indicator Panel, standard GPIB interfacing, and robust pattern-detection capabilities for PRBS and programmable word patterns.

SPECIFICATIONS

General

Interfaces	GPIB
Display	Fluorescent Indicator Panel (FIP)
Product Type	Error Detector

Physical

Power Supply	100 to 120 V AC / 200 to 240 V AC, 47.5 to 63 Hz, <= 300 VA
Operating Temperature	0 to +40 °C °C
Dimensions (W×H×D)	426 mm × 177 mm × 451 mm mm
Weight	24 kg kg

Receiver Performance

Bit Rate Range	50 Mbit/s to 12.5 Gbit/s
Clock Frequency Range	50 MHz to 12.5 GHz

Input Characteristics

Data Input Impedance	50 Ω
Clock Input Impedance	50 Ω
Data Input Connector	SMA female
Clock Input Connector	SMA female
Data Input Amplitude Range	0.1 Vp-p to 2.0 Vp-p
Clock Input Amplitude Range	0.5 Vp-p to 2.0 Vp-p

Pattern Detection

PRBS Patterns	2^7 - 1, 2^9 - 1, 2^11 - 1, 2^15 - 1, 2^20 - 1, 2^23 - 1, 2^31 - 1
Word Pattern Length	2 to 65,536 bits

Measurements

Measurement Modes	Total error, Error rate, Error interval, Error-free interval
Gating Modes	Single, Continuous, Manual, External
Gating Time	1 s, 10 s, 100 s (or manual settings)

Features

Auto-Search Function	Supported (threshold, phase, threshold and phase)
Sync Control	Auto or Manual

Measurement & Analysis

Error Performance Analysis Standards	ITU-T G.821

Options

1/4 Clock Input (Option 01)	Enables operation with a clock frequency at 1/4 of the data bit rate (12.5 MHz to 3.125 GHz)

Inputs & Outputs

Gating Input	TTL level, BNC connector (rear panel)
Gating Output	TTL level, BNC connector (rear panel)

TECHNOLOGY

Bit Error Rate Testing (BERT)

APPLICATIONS

High-speed digital transmission and optical communication network testing

Generated by SpecAtlasHub (specatlashub.com). Specifications are compiled from available sources and are subject to change — verify critical values against the manufacturer before purchase.