

KEYSIGHT TECHNOLOGIES (AGILENT HP)

16700B

The Keysight Technologies (Agilent HP) 16700B is a powerful logic analysis system mainframe optimized for comprehensive system-level debugging of complex multi-processor and bus designs. Offering a modular architecture with five expansion slots, it enables design teams to customize and scale their hardware and software integration testing environments.

SPECIFICATIONS

General

Display	External monitor required
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Physical

Power Supply	115/230 V, 48 to 66 Hz, 610 W max
Operating Temperature	0 °C to 50 °C °C
Dimensions (W×H×D)	426 mm × 337 mm × 442 mm mm
Weight	12.7 kg kg

System Architecture

Number of Module Slots	5 measurement module slots (including 1 slot configurable for emulation or Multiframe module)
CPU	HP PA-RISC 7300LC (150 MHz)
Emulation Module Slots	1
Expansion Frame Support	Supports one 16701B expansion frame (adding 5 measurement module slots)

Processor & Data Storage

System RAM	128 MB (upgradeable to 256 MB with Option 003)
Hard Disk Drive Capacity	18 GB
Removable Storage Options	Built-in 24x CD-ROM drive

Connectivity & Interfaces

LAN Interface Port	10/100 BaseT auto-sensing RJ-45
Target Control Interface Port	Ten-conductor, flying lead cable

Electrical & Power

AC Input Voltage Range	115/230 V
AC Input Frequency Range	48 to 66 Hz
Maximum Power Consumption	610 W

Interfaces

HP-IB Interface	IEEE 488.2 active controller
Parallel Port	25-pin D-sub, Centronics-compatible
Video Output Port	15-pin VGA, up to 1600 × 1200 resolution
PS/2 Ports	Two ports (dedicated mouse and keyboard)
BNC I/O Ports	Two configurable BNC connectors (Port 1 and Port 2)

Software

Operating System	HP-UX
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Environmental

Storage Temperature Range	-40 °C to +75 °C
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Performance

System Time Correlation Resolution	2 ns
Intermodule Trigger Lines	8
Maximum State/Timing Channels	Up to 1020 (configuration and module dependent)

Software & Connectivity

Remote Control Protocols	X-Window (X11), FTP, TCP/IP, GPIB (HP-IB)
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Compliance

Safety Standards	CSA C22.2 No. 1010.1, EN 61010-1 / IEC 1010-1
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TECHNOLOGY

Logic Analyzer Mainframe

APPLICATIONS

System-level hardware/software integration and bus debugging

Generated by SpecAtlasHub (specatlashub.com). Specifications are compiled from available sources and are subject to change — verify critical values against the manufacturer before purchase.