KEYSIGHT TECHNOLOGIES (AGILENT HP)

1672G

The Agilent Keysight 1672G is a high-performance standalone logic analyzer featuring 68 channels of logic analysis. Designed for debugging digital circuits, it offers a state speed of 150 MHz and a timing speed of 500 MHz. The system comes equipped with 64K of standard acquisition memory, with expansion options up to 256K (Option 001) or 2M (Option 002). Key configuration options include an integrated 2-channel, 500 MHz oscilloscope (Option 003) and a 32-channel pattern generator (Option 004). The unit features robust connectivity through GPIB, RS-232, and LAN ports, and stores data on a built-in 2 GB hard drive or a 1.44 MB floppy drive.

SPECIFICATIONS

General

Interfaces	LAN (10Base-T), GPIB, RS-232, Centronics parallel printer port, PS/2 keyboard, PS/2 mouse, VGA external video output
Display	Color flat-panel display with VGA resolution
Supported File Systems	LIF, DOS

Physical

Operating Temperature	0 to +40 °C °C
Dimensions (W×H×D)	381 mm × 186 mm × 421 mm mm
Weight	12.7 kg (14.1 kg with option 003 or 004) kg

Acquisition Performance

Base Channel Count	68
Maximum State Clock Rate	150 MHz
Maximum Timing Analysis Sample Rate	500 MHz
Base Memory Depth (Standard)	64K
Memory Depth (Option 001)	256K
Memory Depth (Option 002)	2M

Triggering and Sequencing

Trigger Functions	23 predefined trigger functions with graphical setup
Trigger Sequencer Levels	12

Probing and Electrical Input

Input Impedance (Resistance)	100 kΩ
Input Impedance (Capacitance)	8 pF
Minimum Input Voltage Swing	500 mV peak-to-peak
Maximum Input Voltage	±40 V

System and Hardware Architecture

Internal Storage Capacity	2 GB HDD
Removable Storage	1.44 MB DOS floppy drive

Connectivity and External I/O

LAN Remote Protocols	X-Windows, FTP, NFS

Software and Analysis Tools

Export Image Formats	PCX, TIFF, EPS
Export Data Formats	ASCII

Option 003 Oscilloscope

Option 003 Oscilloscope Channels	2
Option 003 Oscilloscope Bandwidth	500 MHz
Option 003 Oscilloscope Sample Rate	2 GSa/s
Option 003 Oscilloscope Memory Depth	32K
Option 003 Oscilloscope Vertical Resolution	8 bits
Option 003 Oscilloscope Input Coupling	AC, DC, 50 Ω

Option 004 Pattern Generator

Option 004 Pattern Generator Channels	32
Option 004 Pattern Generator Vector Speed	100M vector/sec
Option 004 Pattern Generator Memory Depth	256K vectors

Signal Input

Threshold Range	-6.0 V to +6.0 V (50 mV increments)
Threshold Accuracy	±(100 mV + 3% of setting)
Input Dynamic Range	±10 V relative to threshold
Preset Thresholds	TTL, CMOS, ECL

State Analysis

Setup/Hold Window	1.0 ns
Minimum Clock Pulse Width	2.0 ns
Number of Clocks	4

Timing Analysis

Transitional Timing Sample Rate	125 MHz
Conventional Timing Sample Rate (Half Channels)	500 MHz
Glitch Detection Minimum Pulse Width	3.0 ns
Timing Zoom Sample Rate	2 GHz
Timing Zoom Memory Depth	64 K samples

Environmental

Non-Operating Temperature	-40 °C to +75 °C
Operating Humidity	Up to 90% RH (non-condensing) at +40 °C

Triggering

Pattern Recognizers	10
Range Recognizers	2
Edge/Transition Recognizers	2
Glitch/Noise Recognizers	2 (in timing mode)
Maximum Trigger Sequence Rate	150 MHz

TECHNOLOGY

Logic Analyzer

APPLICATIONS

Digital Debugging and Mixed-Signal Analysis

Generated by SpecAtlasHub (specatlashub.com). Specifications are compiled from available sources and are subject to change — verify critical values against the manufacturer before purchase.