

## KEYSIGHT TECHNOLOGIES (AGILENT HP)

# 16803A

The Agilent/Keysight 16803A is a high-performance 102-channel portable logic analyzer designed to tackle demanding digital debug challenges. Featuring 4 GHz timing zoom with 250 ps resolution and 64K deep memory, standard 250 MHz state analysis (upgradable to 450 MHz), and standard 1 M deep acquisition memory (upgradable up to 32 M), the 16803A delivers reliable precision across both timing and state domains. It features an integrated 15-inch color XGA display, seamless analog-digital integration via View Scope, and a rich suite of hardware triggering resources.

## SPECIFICATIONS

### General

|         |                                                                   |
|---------|-------------------------------------------------------------------|
| Display | 15-inch color LCD, 1024 × 768 pixels (XGA), optional touch screen |
|---------|-------------------------------------------------------------------|

### State Acquisition

|                                        |                               |
|----------------------------------------|-------------------------------|
| State Mode Channels                    | 102                           |
| Standard State Clock Rate              | 250 MHz                       |
| Maximum State Clock Rate (Upgradeable) | 450 MHz (with Option 250)     |
| Standard State Data Rate               | 250 Mb/s                      |
| Maximum State Data Rate (Upgradeable)  | 500 Mb/s (with Option 250)    |
| Standard State Memory Depth            | 1 M sample                    |
| Maximum State Memory Depth             | 32 M sample (with Option 032) |

### Timing Acquisition

|                                           |                                                                             |
|-------------------------------------------|-----------------------------------------------------------------------------|
| Timing Mode Channels                      | 102                                                                         |
| Maximum Timing Sample Rate (Half-Channel) | 1.0 GHz (1.0 ns)                                                            |
| Maximum Timing Sample Rate (Full-Channel) | 500 MHz (2.0 ns)                                                            |
| Timing Zoom Sample Rate                   | 4.0 GHz (250 ps)                                                            |
| Timing Zoom Memory Depth                  | 64 K sample                                                                 |
| Standard Timing Memory Depth              | 1 M sample                                                                  |
| Maximum Timing Memory Depth Options       | 2 M (Opt 002), 4 M (Opt 004), 8 M (Opt 008), 16 M (Opt 016), 32 M (Opt 032) |

### Triggering

|                                             |                           |
|---------------------------------------------|---------------------------|
| Number of Trigger Levels                    | 10                        |
| Standard Trigger Sequence Rate              | 250 MHz                   |
| Maximum Trigger Sequence Rate (Upgradeable) | 450 MHz (with Option 250) |
| Maximum Trigger Sequence Levels             | 10                        |

### Host System and User Interface

|                    |                        |
|--------------------|------------------------|
| Touchscreen Option | Available (Option 103) |
|--------------------|------------------------|

Connectivity and I/O Interfaces

|                        |                               |
|------------------------|-------------------------------|
| USB Ports              | 6 × USB 2.0 (2 front, 4 rear) |
| LAN Interface          | 10/100/1000 Base-T RJ-45      |
| External Video Outputs | 15-pin VGA                    |
| Trigger Input          | BNC, female                   |
| Trigger Output         | BNC, female                   |

Connectivity

|                                         |   |
|-----------------------------------------|---|
| Number of 90-pin Probe Cable Connectors | 3 |
|-----------------------------------------|---|

Configuration

|                       |                                    |
|-----------------------|------------------------------------|
| Channel Configuration | 98 data channels, 4 clock channels |
|-----------------------|------------------------------------|

Inputs & Outputs

|                          |                                       |
|--------------------------|---------------------------------------|
| External Video Interface | 15-pin D-Sub (VGA)                    |
| USB Port Layout          | 2 front ports, 4 rear ports (USB 2.0) |
| LAN Interface Speed      | 10/100/1000 Base-T (Gigabit Ethernet) |

TECHNOLOGY

Logic Analysis

APPLICATIONS

Digital hardware debug, embedded systems verification, timing and state analysis

Generated by SpecAtlasHub (specatlashub.com). Specifications are compiled from available sources and are subject to change — verify critical values against the manufacturer before purchase.