TEKTRONIX

P3420

The Tektronix P3420 is a 4-channel Variable-Delay Data Pod designed for use with the Tektronix HFS 9000 Series Stimulus System. It supports operation up to 640 MHz and offers flexible, programmable output levels, making it highly suitable for testing high-speed digital designs, including ECL, PECL, and CMOS logic families.

SPECIFICATIONS

General

Interfaces	Proprietary mainframe interface connector, 4 x SMA output connectors
Product Type	Variable-Delay Data Pod

Physical

Power Supply	Powered directly by host HFS 9000 mainframe
Operating Temperature	10 °C to 40 °C °C
Dimensions (W×H×D)	89 mm × 25 mm × 127 mm mm
Weight	0.37 kg kg
Cable Length	1.5 m (5 ft)

System Integration

Compatible Mainframes	Tektronix HFS 9000 Series (HFS 9003, HFS 9009)

Output Channels

Number of Channels	4
Output Connector Type	SMA
Output Impedance	50 Ω
High-Level Output Voltage Range	-2.0 V to +5.5 V (into 50 Ω)
Low-Level Output Voltage Range	-2.0 V to +5.5 V (into 50 Ω)
Minimum Output Amplitude	250 mV
Maximum Output Amplitude	7.5 V
Voltage Level Resolution	10 mV

Timing & Clock

Maximum Operating Frequency	640 MHz

Performance

Rise/Fall Time	≤ 250 ps (20% to 80%)
Delay Resolution	1 ps
Minimum Pulse Width	780 ps
Delay Range	0 to 10 ns
Channel-to-Channel Skew	< 250 ps (with identical settings)
Output Aberration	≤ 10% (for 1 V p-p swing)

APPLICATIONS

High-speed digital design testing and logic characterization

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