

TEKTRONIX

TLA7012

The Tektronix TLA7012 is a 2-slot portable logic analyzer mainframe featuring an integrated 15.1-inch XGA display, touchscreen, and built-in Windows XP Professional PC controller. Designed for digital system debug, microprocessor analysis, and signal integrity testing, the TLA7012 accepts TLA7000-series logic analyzer and pattern generator modules. It supports cross-domain analysis via Integrated View (iView™) with Tektronix oscilloscopes and can operate as a standalone system or linked in multi-mainframe expansion configurations.

SPECIFICATIONS

General

Display	15.1 in (383 mm) Active-Matrix Color TFT LCD, 1024 x 768 XGA, Touchscreen
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Physical

Power Supply	100 to 240 V AC, 50/60 Hz; 115 V AC, 400 Hz; 600 W max
Operating Temperature	+5 °C to +45 °C °C
Dimensions (W×H×D)	451 x 295 x 460 mm mm
Weight	14.5 kg kg

System Architecture & Expansion

Number of Module Slots	2 module slots
Mainframe Configurations	Operates as Master or Expansion mainframe
Maximum Channels per Mainframe	Up to 272 channels (136 channels per slot)

Display & User Interface

Display Size	15.1 in (383 mm) diagonal
Display Resolution	1024 x 768 pixels (XGA)
Display Type	Active-matrix color TFT LCD
Touchscreen Capability	Standard integrated touchscreen
External Display Interface	SVGA and DVI monitor outputs

Embedded Processing & System

Operating System	Microsoft Windows XP Professional
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Connectivity & External I/O

Ethernet Interface	10/100/1000Base-T (RJ-45)
USB Ports	USB 2.0 ports (front and rear panels)

Power Supply Specifications

Input Voltage Range	100 V AC to 240 V AC
Input Frequency Range	50 Hz to 60 Hz (100–240 V) / 400 Hz (115 V)
Maximum Power Consumption	600 W

Physical Characteristics

Form Factor	Portable mainframe with integrated display, controls, and handle
Height	295 mm
Width	451 mm
Depth	460 mm
Empty Mainframe Weight	14.5 kg

Environmental & Safety

Operating Temperature Range	+5 °C to +45 °C
Storage Temperature Range	-20 °C to +60 °C

TECHNOLOGY

Modular Logic Analyzer

APPLICATIONS

Digital System Debug, Embedded Systems Analysis, Bus and Protocol Decoding, Pattern Generation

Generated by SpecAtlasHub (specatlashub.com). Specifications are compiled from available sources and are subject to change — verify critical values against the manufacturer before purchase.