

TEKTRONIX

TLA714

The Tektronix TLA714 is a color portable logic analyzer mainframe. It acts as the configurable base for a portable test instrument, holding up to four C-size instrument modules from the TLA 700 Series. Powered by a 266 MHz Intel Pentium with MMX processor and running a Windows-based operating system, it integrates logic analysis and digital storage oscilloscope capabilities with a built-in color flat-panel display and standard PC-like connectivity.

SPECIFICATIONS

General

Interfaces	1x USB, 2x PCMCIA PC card slots, 10/100 Base-T LAN, SVGA external video, Centronics Parallel, RS-232 Serial, 2x PS/2
Display	10.4 in. color flat-panel active-matrix TFT LCD, 800 x 600 SVGA
Warm-up Time	30 minutes

Physical

Power Supply	90 to 250 V AC, 45 to 66 Hz
Operating Temperature	+5 °C to +50 °C °C
Dimensions (W×H×D)	445 mm × 343 mm × 457 mm mm
Weight	14.5 kg

System Architecture

Processor	Intel Pentium with MMX
Processor Clock Speed	266 MHz
System Memory (RAM)	128 MB (expandable)
Standard Operating System	Windows 98 / Windows 2000
PCMCIA Slots	Two PC Card slots

Storage

Floppy Drive	Built-in 3.5 in. 1.44 MB
CD-ROM Drive	Built-in CD-ROM

Chassis Capacity

Number of Module Slots	4
Slot Size	C-size
Module Compatibility	TLA 700 Series instrument modules

Clocking

CLK10 Frequency	10 MHz
CLK10 Accuracy	±100 PPM

System Trigger Input

System Trigger Input Connector	Rear-panel BNC
System Trigger Input Destination	System trigger (TTLTRG7)
System Trigger Input VIH	≥ 2.0 V
System Trigger Input VIL	≤ 0.8 V
System Trigger Input Mode	Falling edge sensitive, latched (active low)
System Trigger Input Minimum Pulse Width	12 ns
System Trigger Input Maximum Voltage	0 to +5 V peak

External Signal Input

External Signal Input Connector	Rear-panel BNC
External Signal Input Destinations	Signal 1, 2 (ECLTRG0, 1), Signal 3, 4 (TTLTRG0, 1)
External Signal Input VIH	≥ 2.0 V
External Signal Input VIL	≤ 0.8 V
External Signal Input Maximum Voltage	0 to +5 V peak

System Trigger Output

System Trigger Output Connector	Rear-panel BNC
System Trigger Output Source	System trigger (TTLTRG7)
System Trigger Output VOH (Open Circuit)	≥ 4 V
System Trigger Output VOH (50 Ohm to Ground)	≥ 2 V
System Trigger Output VOL (Sinking 10 mA)	≤ 0.7 V
System Trigger Output Source Mode	Falling edge sensitive
System Trigger Output Protection	Short-circuit protected (to ground)

External Signal Output

External Signal Output Connector	Rear-panel BNC
External Signal Output Sources	Signal 1, 2 (ECLTRG0, 1), Signal 3, 4 (TTLTRG0, 1), 10 MHz clock (CLK10)
External Signal Output VOH (Open Circuit)	≥ 2.0 V
External Signal Output VOH (50 Ohm to Ground)	≥ 1.0 V
External Signal Output VOL (Sinking 10 mA)	≤ 0.4 V (Sinking 15 mA)
External Signal Output Modes	User-definable: active low or active high
External Signal Output Protection	Short-circuit protected (to ground)

Connectivity

Parallel Port Type	Centronics LPT (DB-25)
Serial Port Type	RS-232-C (DB-9)
Audio IO	3.5 mm microphone in, line out

Storage & Expansion

PCI Expansion Slots	1x half-length PCI slot
Hard Disk Drive	10 GB or larger (IDE, configuration dependent)

External Connections

PS/2 Keyboard Port	1x PS/2
PS/2 Mouse Port	1x PS/2
External Display Output	15-pin D-sub (SVGA)

Trigger & Signal Latency

Module Trigger to System Trigger Output Latency	45 ns
Module Trigger to External Signal Output Latency	60 ns
System Trigger Input to Module Trigger Latency	30 ns
External Signal Input to Module Trigger Latency	45 ns

Environmental

Non-Operating Temperature	-20 °C to +60 °C
Operating Humidity	20% to 80% relative humidity (non-condensing)
Operating Altitude	Up to 3,000 m
Non-Operating Altitude	Up to 12,190 m
Operating Vibration	0.22 g RMS (5 Hz to 500 Hz)
Cooling Clearance	51 mm (2 in) on sides and rear

System

VXIbus Specification Compliance	VXIbus Specification Revision 1.4
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Compliance

Safety Standards	UL3111-1, CAN/CSA-C22.2 No. 1010.1, EN 61010-1
EMC Standards	EN 50081-1, EN 50082-1, FCC Part 15 Class A

TECHNOLOGY

Logic Analyzer

APPLICATIONS

Hardware debugging, digital system analysis, state/timing measurements, and oscilloscope integration

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