

TEKTRONIX

TLA7AA3

The Tektronix TLA7AA3 is a high-performance 102-channel logic analyzer module for TLA7000 series mainframes. Featuring MagniVu acquisition technology with up to 125 ps (8 GHz) timing resolution, up to 800 MHz state acquisition, and deep memory up to 32 Mb per channel, the TLA7AA3 provides comprehensive timing, state, and simultaneous analog/digital signal analysis for complex embedded systems debugging.

SPECIFICATIONS

General

Interfaces	P6800/P6900 series probe connectors, TLA mainframe backplane interface
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Physical

Power Supply	Powered directly via TLA7000 series mainframe backplane
Operating Temperature	5 to 50 °C °C
Module Slot Width	Double-wide module (requires 2 mainframe slots)

General Architecture & Performance

Module Type	Logic Analyzer Module
Mainframe Compatibility	Tektronix TLA7000 Series (e.g. TLA7012, TLA7016)
Number of Channels	102 channels (96 data, 6 clock/qualifier)
Max Asynchronous Acquisition Rate	8 GHz (MagniVu)
Memory Depth Options	Up to 32 Mb per channel

Timing Acquisition Specifications

Max Timing Sample Rate	8 GHz (MagniVu) / 2 GHz (Deep Timing)
Timing Resolution	125 ps (MagniVu)
Half-Channel Timing Sample Rate	2 GHz (500 ps resolution)
MagniVu Memory Depth	16 Kb per channel
Transitional Storage	Supported

Triggering & Event Recognition

Trigger Sequence States	16
Pattern Recognizers	16
Range Recognizers	4
Timers	2
Event Counters	2
Glitch Trigger Capabilities	Supported
Setup/Hold Violation Triggering	Supported
Cross-Module Triggering	Supported via backplane

Probing & Signal Inputs

Probe Capacitive Loading	0.5 pF (with P68xx/P69xx compression probes)
Probing System	Compression Probing System
Simultaneous Analog and Digital Analysis	Supported through the same probe pin

Software & Protocol Decoding

Bus Protocol Decode Support	Broad processor and bus support
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Physical & Electrical Specifications

Mainframe Slot Requirement	2 slots
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Inputs & Signal Handling

Minimum Signal Swing	300 mV p-p
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TECHNOLOGY

Logic Analyzer

APPLICATIONS

Digital Logic Analysis & Embedded Systems Debugging

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